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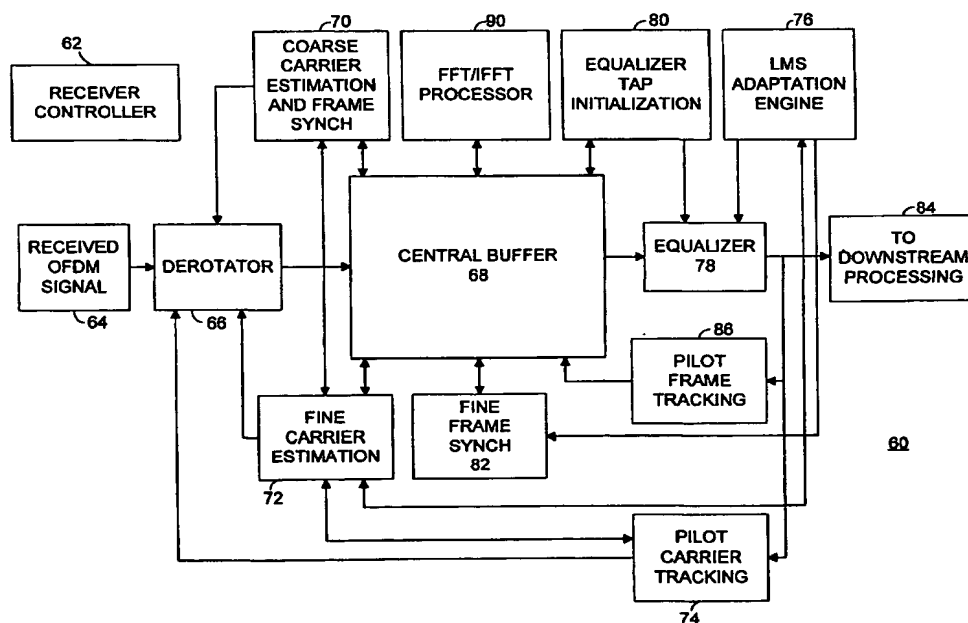
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(54) Title: CENTRALIZED BUFFER ARCHITECTURE FOR AN ORTHOGONAL FREQUENCY DIVISION MULTIPLEXING (OFDM) RECEIVER



(57) Abstract: The disclosed embodiments relate to a centralized buffer architecture (60) for an Orthogonal Frequency Division Multiplexing (OFDM) receiver. In this architecture, a central buffer (68) is shared by the main functional blocks or modules (70-90) of the OFDM receiver. A state machine (62) enables access to the buffer (68) by the functional blocks (70-90) to maintain buffer coherency.